

Fig IA

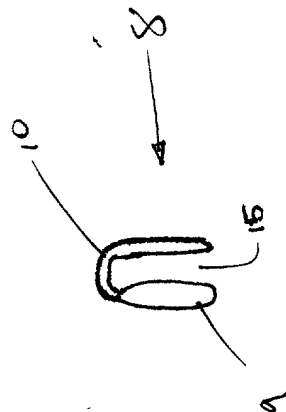


FIGURE 1B

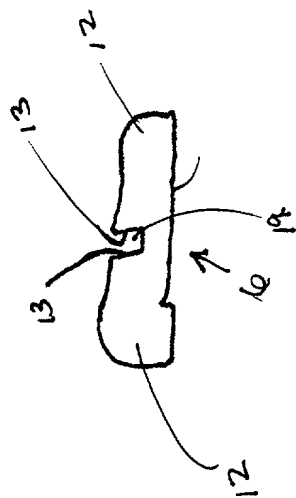


FIGURE 1C

FIG. 2A is a schematic diagram of a circuit 1. The circuit 1 includes a power source 2, a switch 3, a resistor 4, a diode 5, and a load 6. The power source 2 is connected to the switch 3, which is connected to the resistor 4. The resistor 4 is connected to the diode 5, which is connected to the load 6. The load 6 is connected back to the power source 2, completing the circuit.

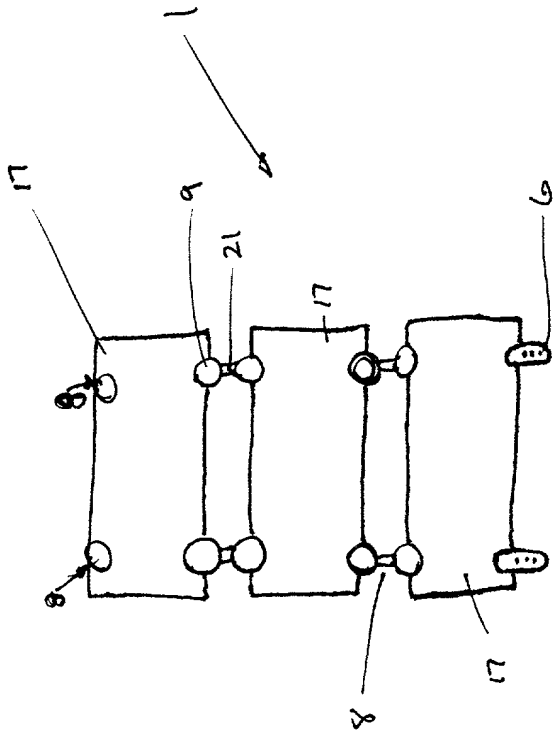


FIG 2A



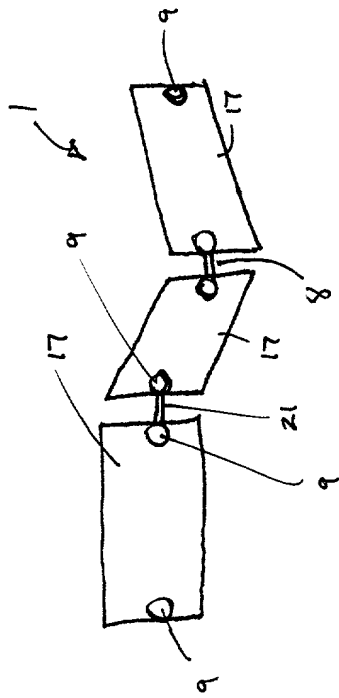


FIG 3 A

FIG. 3B is a side view of the device of FIG. 3A, showing the device in a closed position. The device includes a handle 17, a spring 8, a latch 10, and a locking mechanism 25. The device is shown in a closed position, with the handle 17 and the latch 10 aligned. The spring 8 is shown in a compressed state, and the locking mechanism 25 is shown in a locked position.

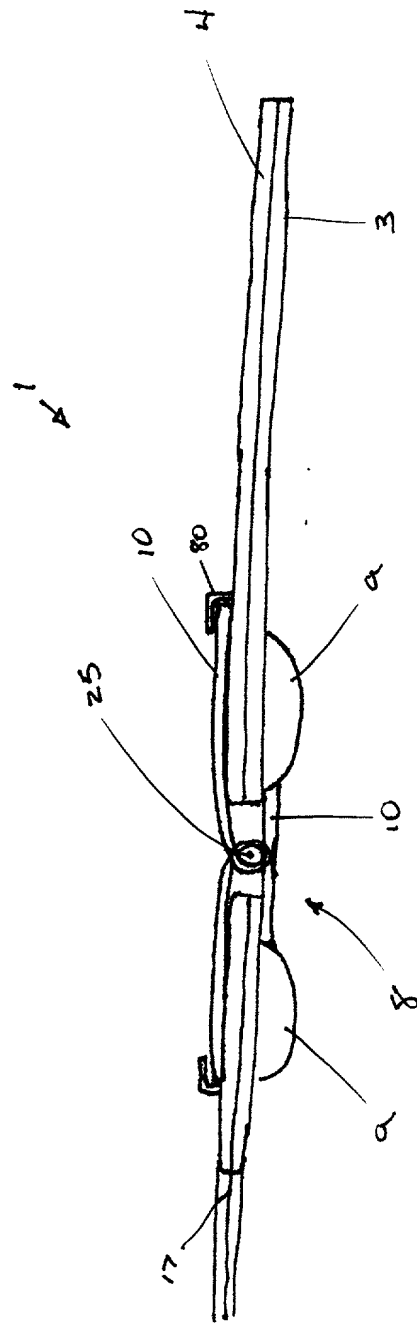


FIG. 3B